


FORM PTO-1449 (Modified)		ATTY. DOCKET NO.	SERIAL NO.:
		IL920030035US1	10/695,970
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT		APPLICANT: Ben-David et al.	
FILING DATE:		GROUP:	
29-Oct-2003		2191	

REFERENCE DESIGNATION U.S. PATENT DOCUMENTS

EXAMINER INITIALS		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPRO.)
MA	AA	6655790	12-16-2003	Glossner et al.			2000-02-29
MA	AB	6915411	7-5-2005	Moreno et al.			2002-07-18
MA	AC	2004-0078554	4-22-2004	Glossner et al.			2003-06-07

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO

OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)

MA	AD	Preeti Ranjan Panda, Nikil D. Dutt, and Alexandru Nicolau. "Efficient utilization of scratch-pad memory in embedded processor applications", in European Design and Test Conf., March 1997
MA	AE	Keith D. Cooper, Timothy J. Harvey. "Compiler controlled Memory", Eighth Int. Conf. on Architectural Support for Programming Languages and Operating Systems, pp. 100-104, Oct, 1998
MA	AF	Masaaki Kondo, Hideki Okawara, Hiroshi Nakamaru, Taisuke Boku. "SCIMA: Software Controlled Integrated Memory Architecture for High Performance Computing", http://citeseer.nj.nec.com/376639.html 2000 MA
MA	AG	William Y. Chen, Roger Bringmann, Scott A. Mahlke, Richard E. Hank, James E. Siculo. "An Efficient Architecture for Loop Based Data Preloading", 25th Annual International Symposium on Microarchitecture (1992) http://citeseer.nj.nec.com/chen92efficient.html
MA	AH	Matthew A. Postiff, Trevor Mudge. "Smart Register Files for High Performance Microprocessors", http://citeseer.nj.nec.com/postiff99smart.html 1999 MA
MA	AI	Matthew A. Postiff, David Greene, Steve Raasch, Trevor Mudge. "Integrating Superscalar Processor Components to Implement Register Caching". http://citeseer.nj.nec.com/468226.html 2001 MA
MA	AJ	Jesus Corbal, Roger Espasa, and Mateo Valero. "Exploiting a new level of DLP in multimedia applications", in Intl. Symposium on Microarchitecture, pages 72-79, 1999.

EXAMINER D

DATE CONSIDERED

2-6-2007

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.